FIG. 1

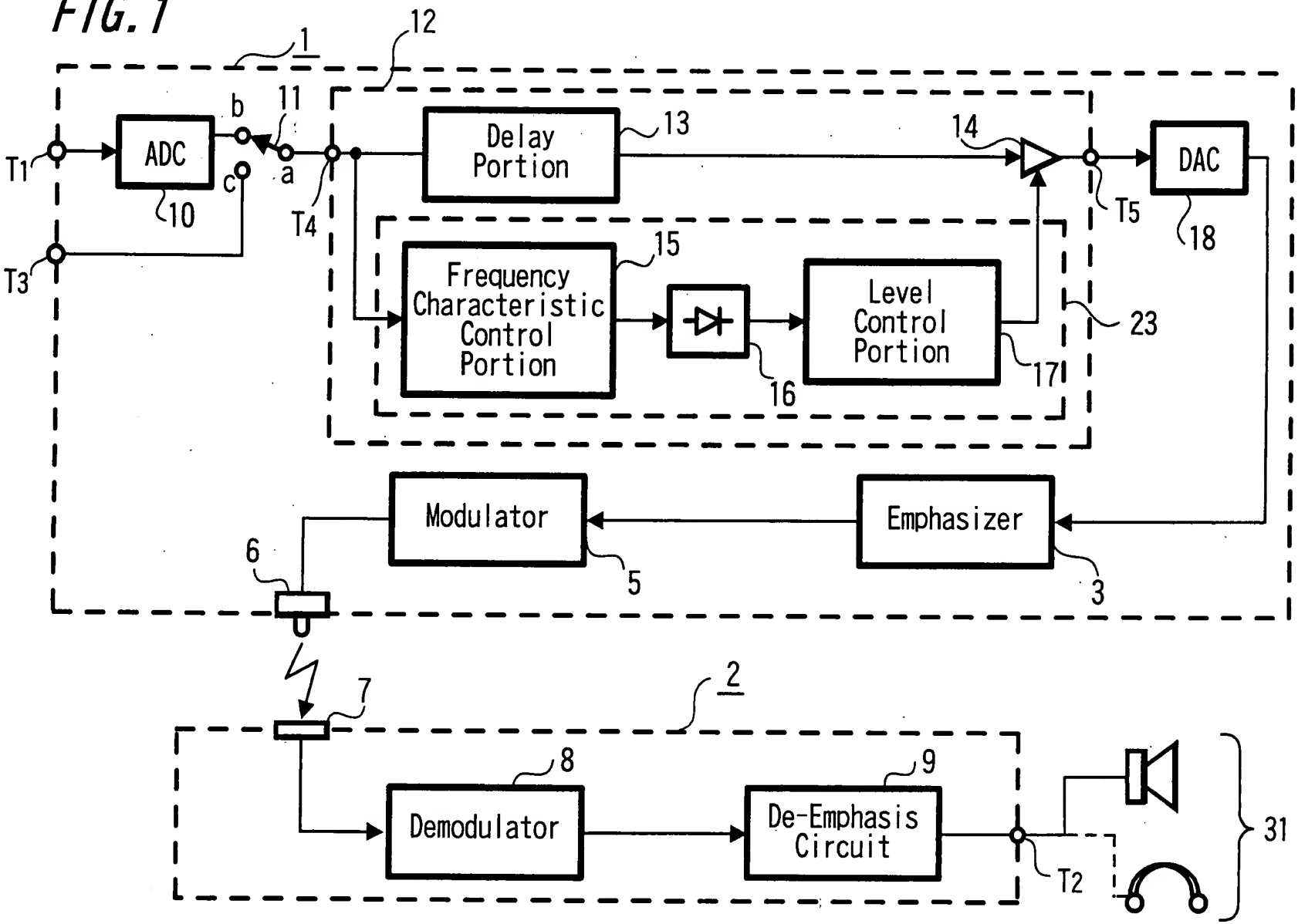


FIG. 2

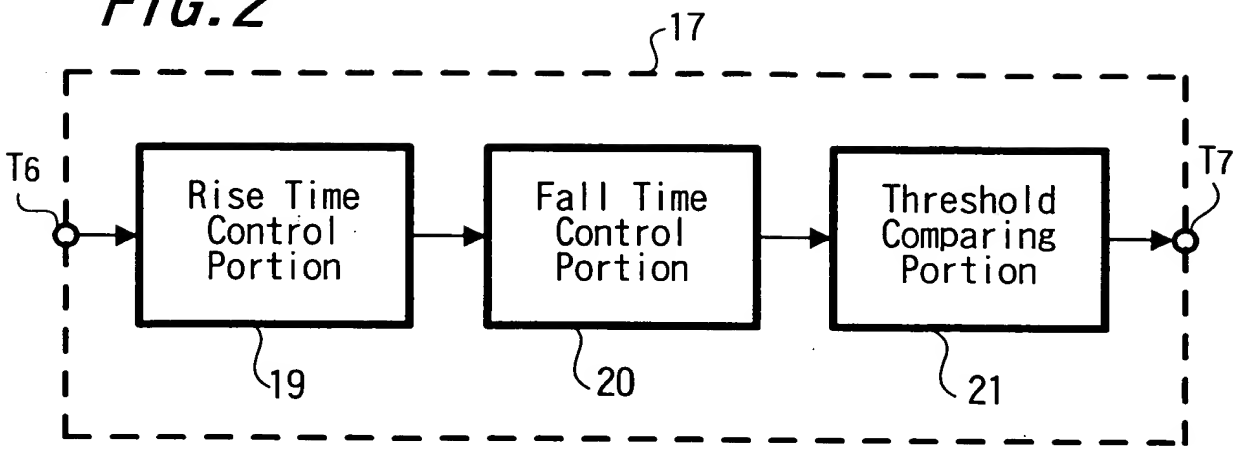

17
T6
Rise Time Control Portion
Fall Time Control Portion
Threshold Comparing Portion
T7
19
20
21

FIG. 3

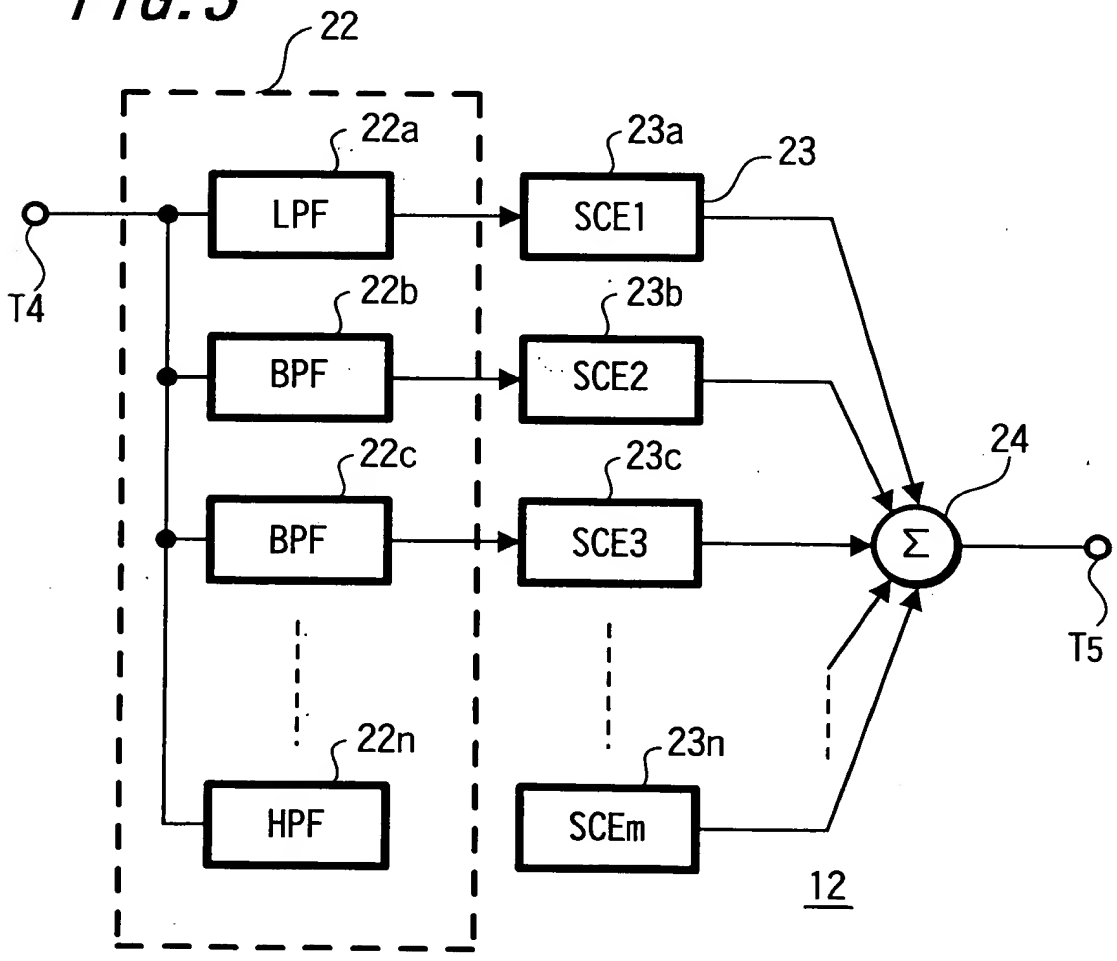

22
22a
23a
23
LPF
SCE1
T4
22b
23b
BPF
SCE2
24
22c
23c
BPF
SCE3
Σ
T5
22n
23n
HPF
SCEm
12

FIG. 4

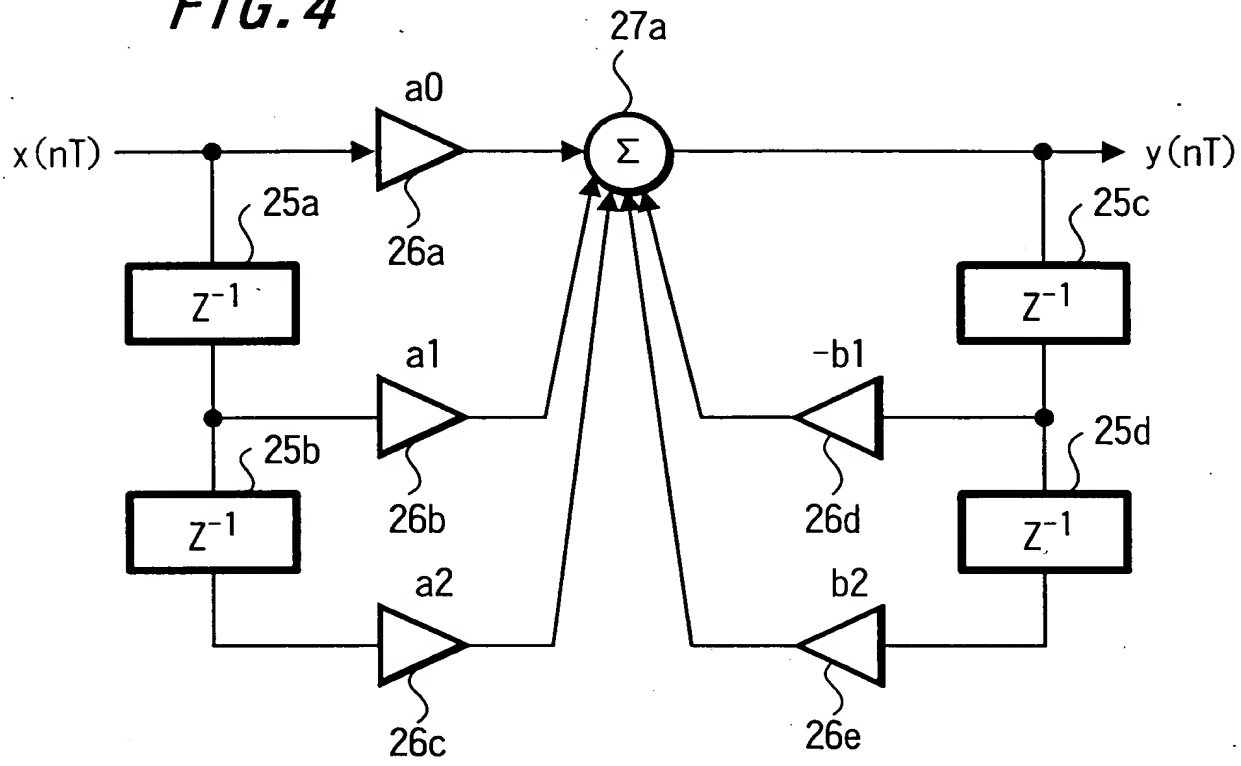


FIG. 5

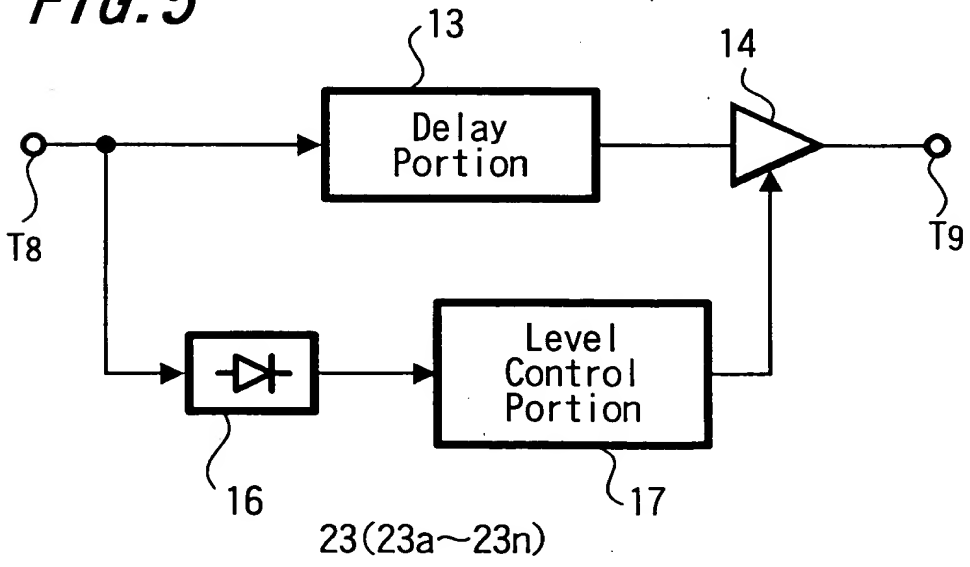
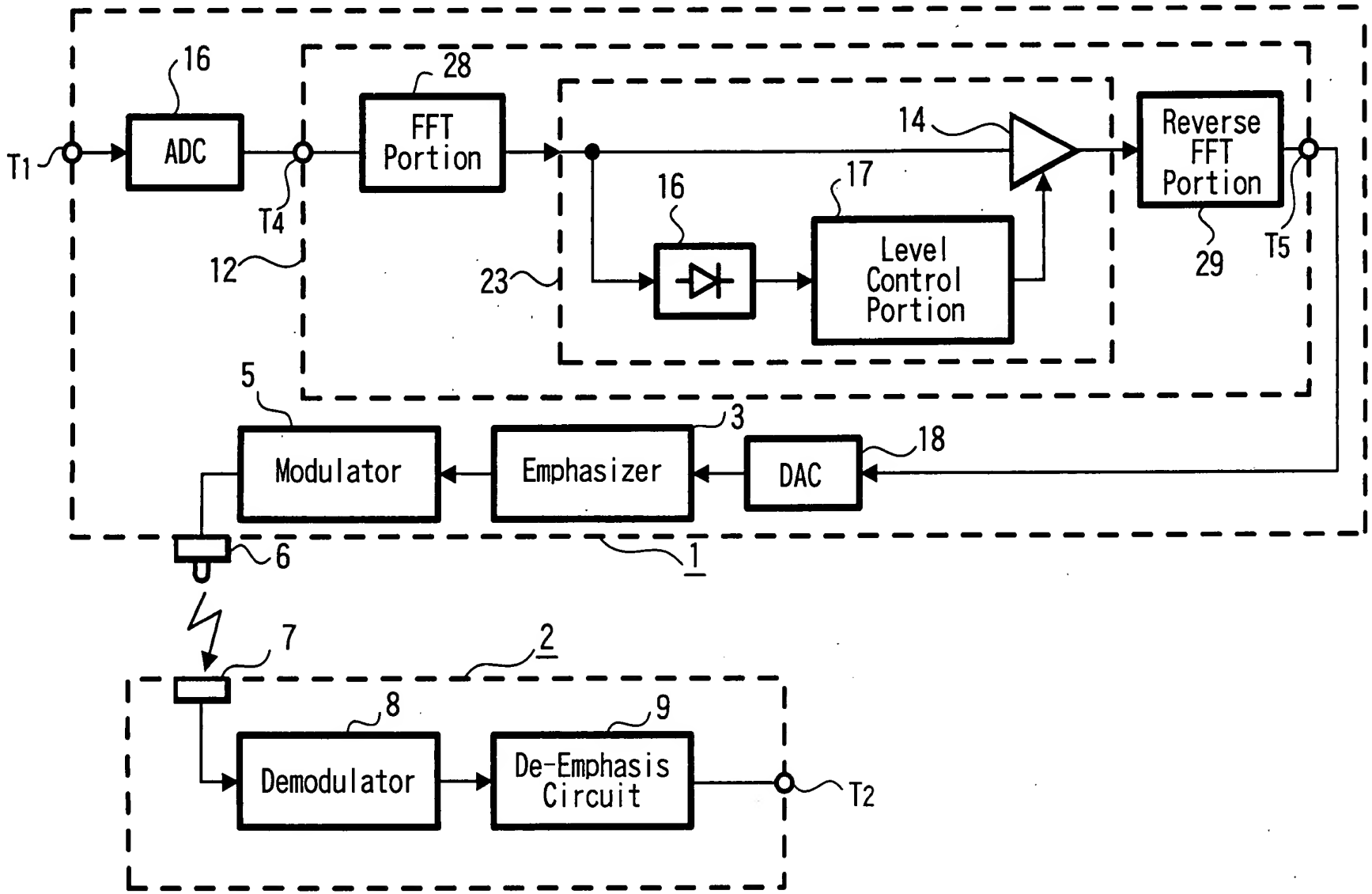


FIG. 6


T1
16
ADC
T4
12
28
FFT Portion
14
16
17
23
Level Control Portion
Reverse FFT Portion
29
T5
5
Modulator
3
Emphasizer
18
DAC
6
1
7
2
8
Demodulator
9
De-Emphasis Circuit
T2

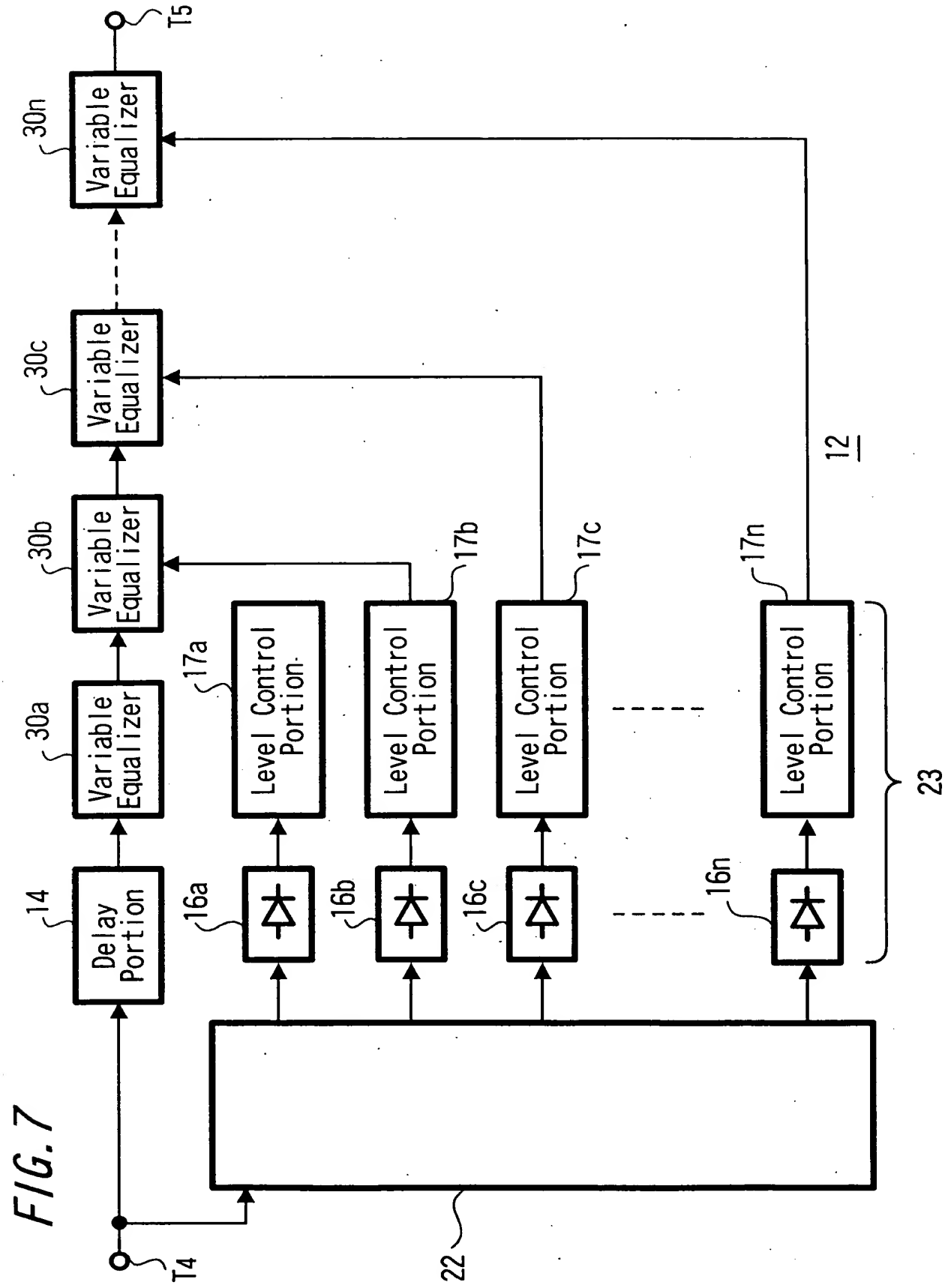


FIG. 7

FIG. 8

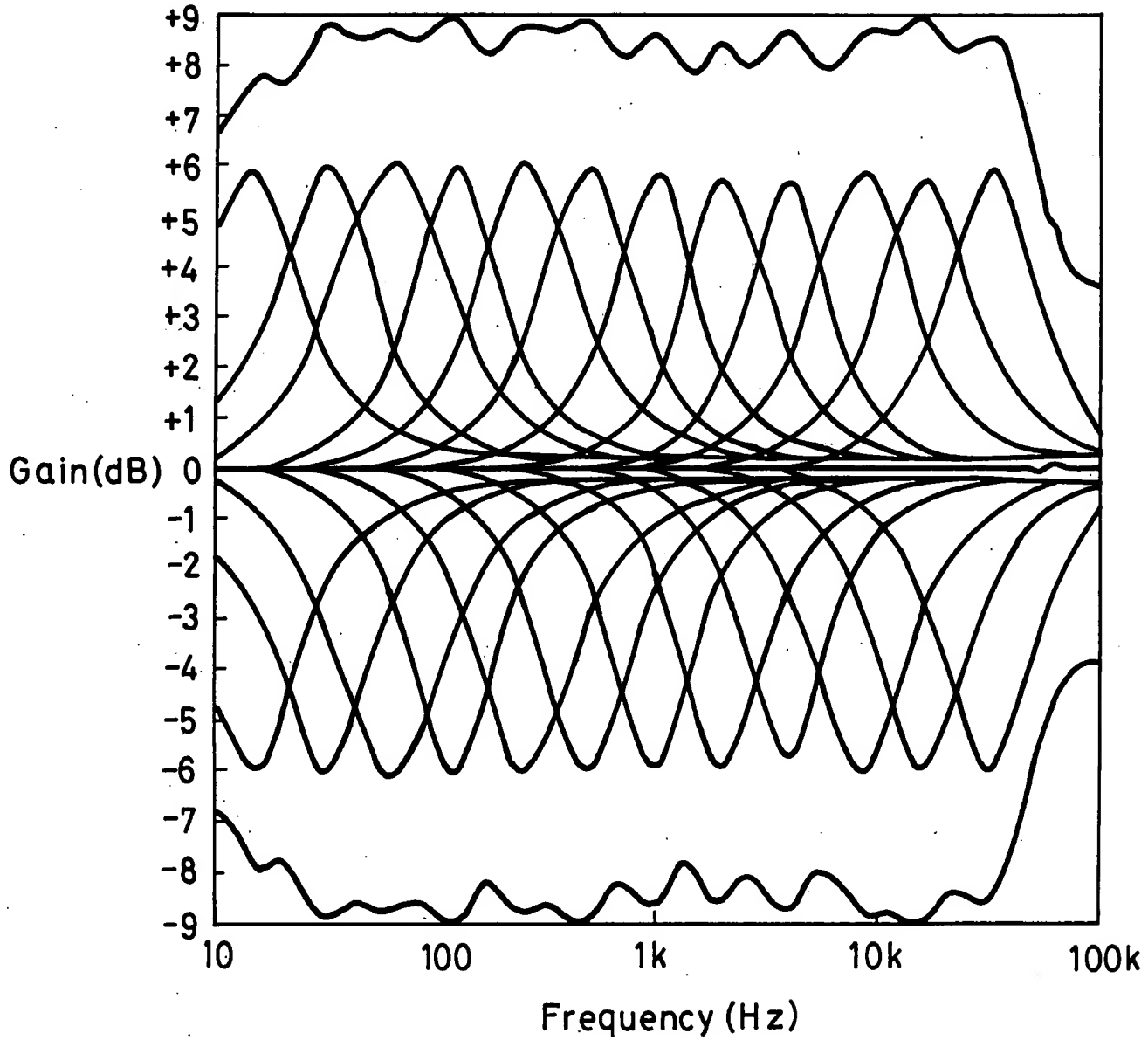

+9
+8
+7
+6
+5
+4
+3
+2
+1
Gain(dB) 0
-1
-2
-3
-4
-5
-6
-7
-8
-9
10
100
1k
10k
100k
Frequency (Hz)

FIG. 9

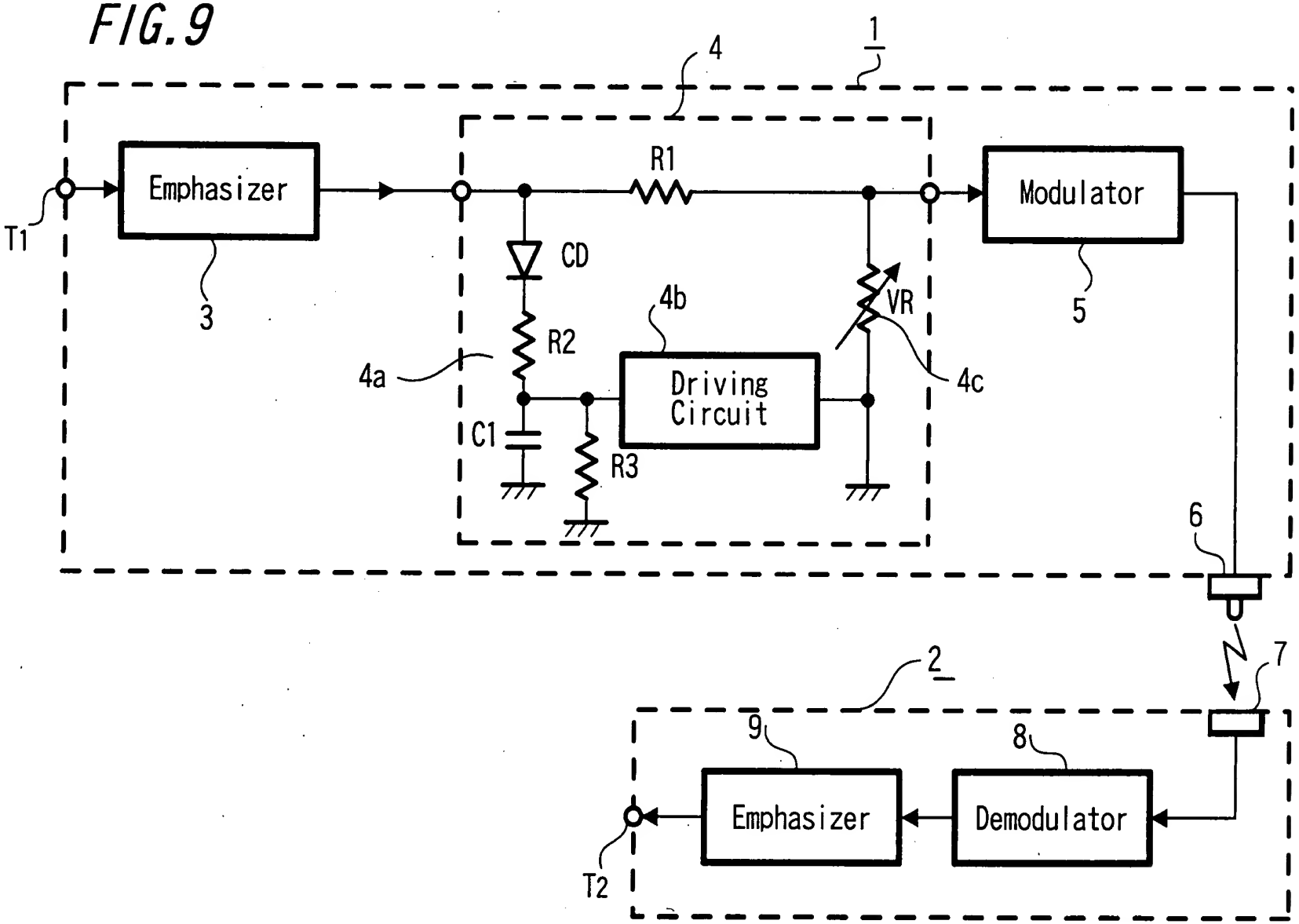
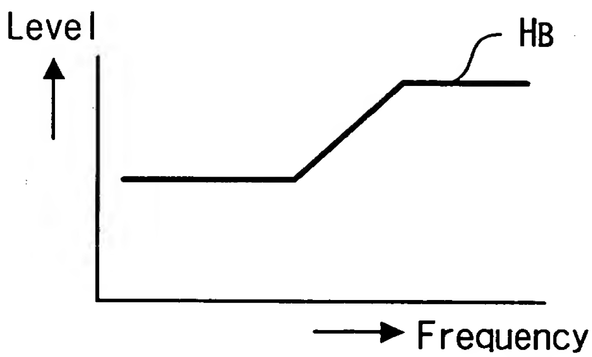
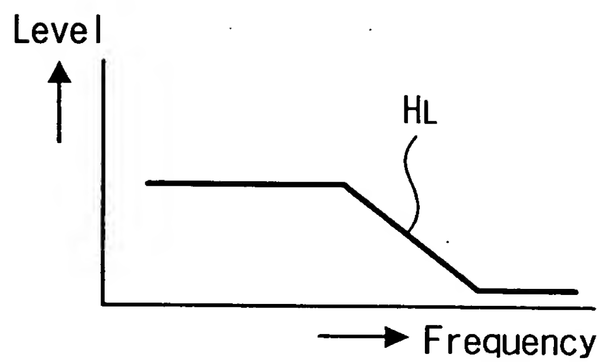

1
4
Emphasizer
T1
3
R1
CD
R2
4a
4b
Driving Circuit
C1
R3
VR
4c
Modulator
5
6
7
2
9
8
Emphasizer
Demodulator
T2

FIG. 10A



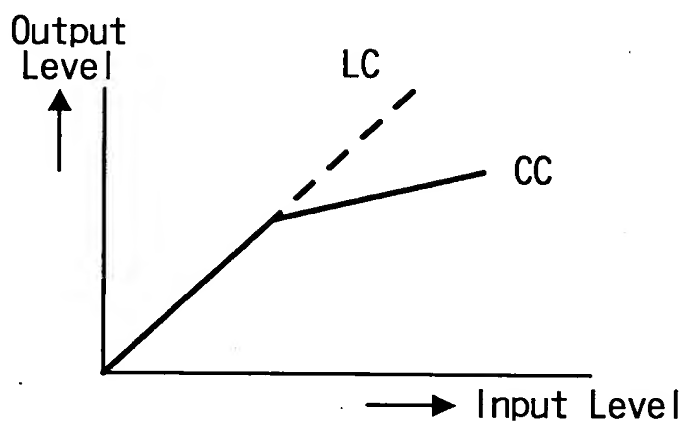
Emphasis Characteristic Diagram

FIG. 10B



De-Emphasis Characteristic Diagram

FIG. 11

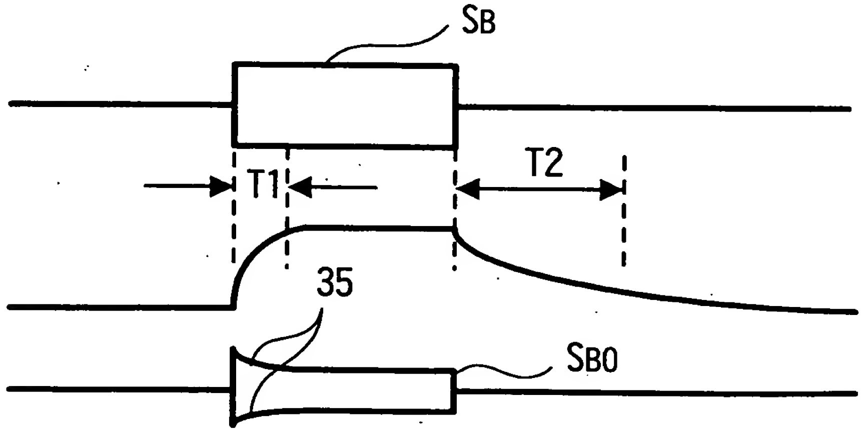


Input/Output Characteristic Diagram

FIG. 12A

FIG. 12B

FIG. 12C



Time Constant